

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	2	("20040250230").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/05/26 10:55
S2	2	("5315535").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/05/26 14:53
S3	2753	wiring adj path	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/26 14:53
S4	897	(board or pcb) and S3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 11:44
S5	24	(signal near3 group\$4) and S4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/26 14:56
S6	5	((divid\$3 or partition\$3) with group\$4) and S5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 16:04
S7	11	(divid\$3 or partition\$3) and group\$4 and S5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/26 15:17
S8	5	(capacitor or cut) and S7	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/26 15:19
S9	28	((number near3 signal) with threshold) same ((divid\$3 or partition\$3) with (unit or group))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 14:36
S10	4	(board or pcb) and S9	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 14:38
S11	46	((number near3 signal) with threshold) same ((divid\$3 or partition\$3) same (unit or group))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 14:38

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S12	8	(board or pcb) and S11	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 14:36
S13	12	"5315535"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 14:30
S14	6	power and S13	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 14:36
S15	5	(divid\$3 or partition\$3) and S13	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 14:36
S16	1	(board or pcb) and S15	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 14:37
S17	217	((number near3 signal) with threshold) same (divid\$3 or partition\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 14:38
S18	18	(board or pcb) and S17	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 14:41
S19	40	(board or pcb or assembly) and S17	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 16:03
S20	27	capacitor and S17	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 14:45
S21	20	power and S20	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 14:47
S22	7	"6086828"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 14:47
S23	0	capacitor and S22	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 14:47

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S24	0	capacitor and S13	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 14:47
S25	24	"3567914"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 16:03
S26	18	(board or pcb or assembly) and S25	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 16:03
S27	13	(divid\$3 or partition\$3) and S26	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 16:05
S28	670863	capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 16:05
S29	1	S27 and S28	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 16:05
S30	70446	(board or pcb) and rout\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 11:45
S31	24117	((group or cluster or unit or bin) with (wir\$3 or connection or interconnection or conductor or signal)) and S30	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/05 09:08
S32	2272	((group or cluster or unit or bin) near4 divid\$3) and S31	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 15:15
S33	471	router and S32	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 14:46
S34	256	adjacent and S33	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 11:49
S35	8	((group or cluster or unit or bin) near4 divid\$3) with (fail\$3 or obstacle or obstruction or barrier or block\$3)) and S34	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 14:47

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S36	5423	(itou.in. or sasaki.in. or okada.in.) and wiring	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 14:25
S37	1781	(board or pcb) and S36	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 15:50
S38	64	(group\$4 with signal) and S37	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 14:28
S39	12	(divid\$3 with group) and S38	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 14:28
S40	4	router and S37	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 14:34
S41	4560	router and S31	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 14:46
S42	2067	((group or cluster or unit or bin) with (fail\$3 or obstacle or obstruction or barrier or block\$3)) and S41	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 16:34
S43	1764	((group or cluster or unit or bin) with (obstacle or obstruction or barrier or block\$3)) and S41	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 14:48
S44	75	((group or cluster or unit or bin) with (obstacle or obstruction or barrier)) and S41	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 14:49
S45	24	((group or cluster or unit or bin) with (divid\$3 or partition\$3)) and S44	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 15:18
S46	6	((group or cluster or unit or bin) near4 partition\$3) and S44	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 15:17
S47	40421	(group or cluster or unit or bin) with (wir\$3 or connection or interconnection or conductor or signal) with (divid\$3 or partition\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 15:49

EAST Search History

S48	20800	(group or cluster or unit or bin) near4 (wir\$3 or connection or interconnection or conductor or signal) with (divid\$3 or partition\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 15:51
S49	2288	(board or pcb) and S48	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 15:50
S50	279	((group or cluster or unit or bin) near6 adjacent) and S49	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 15:51
S51	92	rout\$3 and S50	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/05 07:58
S52	54	path and S51	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 15:53
S53	13	(robust or rough\$2) and S52	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/05 08:47
S54	34	((group or cluster or unit or bin) with (fail\$3 or obstacle or obstruction or barrier or block\$3)) and S52	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 16:34
S55	6	((group or cluster or unit or bin) with (fail\$3 or obstacle or obstruction or barrier)) and S52	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/02 16:34
S56	2990	(smaller near4 (divid\$3 or partition\$3)) and board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/05 08:32
S57	1262	rout\$3 and S56	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/05 08:49
S58	860	(layout or lay\$3) and S57	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/05 07:59
S59	630	path and S58	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/05 07:59

EAST Search History

S60	341	(congestion or density) and S59	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/05 08:01
S61	265	adjacent and S60	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/05 08:01
S62	21	(smaller with adjacent) and S60	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/05 08:07
S63	75	(smaller same adjacent) and S60	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/05 08:29
S64	55	((printed adj2 board) or pcb) and S63	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/05 08:08
S65	91	(smaller same adjacent) and S59	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/05 08:30
S66	5330	(smaller with (divid\$3 or partition\$3)) and board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/05 08:44
S67	3	(congestion with degree) and S66	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/05 08:34
S68	5431	(smaller with (divid\$3 or partition\$3)) and (board or pcb)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/05 08:48
S69	1147	(robust or rough\$2) and S68	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/05 08:47
S70	452	(smaller with (divid\$3 or partition\$3) with (wir\$3 or connection or interconnection or conductor or signal)) and (board or pcb)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/05 08:48
S71	106	rout\$3 and S70	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/05 08:50

EAST Search History

S72	72	adjacent and S71	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/05 08:50
S73	44	((group or cluster or unit or bin) with (wir\$3 or connection or interconnection or conductor or signal)) and S72	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/05 11:26

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	"20040250230"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 13:59
L2	0	printed and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:00
L3	2	wiring and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:05
L4	1	layer and 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:09
L5	0	harness and 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:17
L6	1	"605" and 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:17
L7	1	"605" and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:19
L8	1	small\$2 and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:25

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L9	1	"405" and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:26
L10	62497	group\$3 adj5 signal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:27
L11	508	group\$3 adj5 signal adj6 divid\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:27
L12	751679	wiring or routing	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:28
L13	92	11 and 12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:28
L14	1379073	board	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:28
L15	34	13 and 14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:38
L16	1	obstacle and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:39

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L17	15	obstacle and 15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:40
L18	18	roust\$3 and 15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:54
L19	14	floor\$8 and 15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:44
L20	11	floor\$8 and 18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:45
L21	12	layout and 18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:45
L22	12	"100" and 21	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:55
L23	17	"100" and 18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:58
L24	83396	wiring adj board	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:59

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L25	3	11 and 24	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 14:59
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IEEE STD IEEE Standard

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- ☐ 1. **A CMOS facsimile video signal processor**
 Nakashima, K.; Tadauchi, M.; Hamada, N.; Sato, K.; Yasunari, K.; Nagayama, Y.; Nagai, K.; Suem
Solid-State Circuits, IEEE Journal of
 Volume 20, Issue 6, Dec 1985 Page(s):1179 - 1184
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- ☐ 2. **Manipulation of text documents in the modified Group 4 domain**
 Shulan Deng; Latifi, S.; Kanai, J.;
Multimedia Signal Processing, 1998 IEEE Second Workshop on
 7-9 Dec. 1998 Page(s):438 - 443
 Digital Object Identifier 10.1109/MMSP.1998.738987
[AbstractPlus](#) | Full Text: [PDF](#)(288 KB) IEEE CNF
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- ☐ 3. **An adaptive DSP processor for high-efficiency computing MPEG-4 video encoder**
 Li-Hsun Chen; Chen, O.T.-C.; Teng-Yi Wang; Chi-Lung Wang;
Circuits and Systems, 2004. ISCAS '04. Proceedings of the 2004 International Symposium on
 Volume 2, 23-26 May 2004 Page(s):II - 157-60 Vol.2
[AbstractPlus](#) | Full Text: [PDF](#)(311 KB) IEEE CNF
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- ☐ 4. **A CMOS codec chip for a cost effective Group 4 Fax system**
 Seok Ho Seo; Hong June Park; Ki Sang Hong; Jae Ho Kim; Yoon Soo Kim; Ki Wung Park;
Consumer Electronics, IEEE Transactions on
 Volume 43, Issue 2, May 1997 Page(s):81 - 91
 Digital Object Identifier 10.1109/30.585525
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- ☐ 5. **Low-bit-rate scalable compression of mobile wireless video**
 Fakeh, R.; Ghani, A.A.A.; Saman, M.Y.M.; Ramli, A.R.;
TENCON 2000. Proceedings
 Volume 2, 24-27 Sept. 2000 Page(s):201 - 206 vol.2
 Digital Object Identifier 10.1109/TENCON.2000.888733
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6. Video compression using fax techniques

Yi-Jen Chiu; Berger, T.;

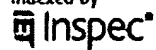
[Data Compression Conference, 1996. DCC '96. Proceedings](#)

31 March-3 April 1996 Page(s):429

Digital Object Identifier 10.1109/DCC.1996.488357

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Nakashima, K.; Tadauchi, M.; Hamada, N.; Sato, K.; Yasunari, K.; Nagayama, Y.; Nagai, K.; Suem
[Solid-State Circuits, IEEE Journal of](#)
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- ☐ 2. **Manipulation of text documents in the modified Group 4 domain**
Shulan Deng; Latifi, S.; Kanai, J.;
[Multimedia Signal Processing, 1998 IEEE Second Workshop on](#)
7-9 Dec. 1998 Page(s):438 - 443
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[Circuits and Systems, 2004. ISCAS '04. Proceedings of the 2004 International Symposium on](#)
Volume 2, 23-26 May 2004 Page(s):II - 157-60 Vol.2
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- ☐ 4. **A CMOS codec chip for a cost effective Group 4 Fax system**
Seok Ho Seo; Hong June Park; Ki Sang Hong; Jae Ho Kim; Yoon Soo Kim; Ki Wung Park;
[Consumer Electronics, IEEE Transactions on](#)
Volume 43, Issue 2, May 1997 Page(s):81 - 91
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[TENCON 2000. Proceedings](#)
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